STUDY MODULE DESCRIPTION FORM

Name of the module/subject	Code
Electronics and Power Electronics	**1010324331010323752**

Field of study	Profile of study (general academic, practical)	Year /Semester
Electrical Engineering	**(brak)**	**2 / 3**
Elective path/specialty	Subject offered in:	Course (compulsory, elective)
-	**Polish**	**obligatory**

Cycle of study:	Form of study (full-time,part-time)
First-cycle studies	**part-time**

No. of hours	No. of credits
Lecture: **20** Classes: **-** Laboratory: **-** Project/seminars: **-**	**3**

Status of the course in the study program (Basic, major, other)	(university-wide, from another field)
(brak)	**(brak)**

Education areas and fields of science and art	ECTS distribution (number and %)
technical sciences	**3 100%**
Technical sciences	**3 100%**

Responsible for subject / lecturer:

dr hab. inż. Michał Gwóźdź
email: michal.gwozdz@put.poznan.pl
tel. 61 665 2646
Faculty of Electrical Engineering
ul. Piotrowo 3A 60-965 Poznań

Prerequisites in terms of knowledge, skills and social competencies:

1	**Knowledge**	Basic knowledge of physics, electrical engineering and mathematical analysis
2	**Skills**	Analysis and synthesis of electrical circuits, carrying out activities in the primary account of operator. The ability to effectively self-education in a field related to the chosen field of study.
3	**Social competencies**	It is aware of the need to broaden their skills and demonstrate their willingness to cooperate within the team

Assumptions and objectives of the course:

Getting to Know the construction, parameters and applications of basic electronic components. Getting to know the principles of operation of analog and digital electronic circuits. The acquisition of electronic design skills at a basic level.

Study outcomes and reference to the educational results for a field of study

Knowledge:

1. Can describe the operating principle and the basic parameters of electronic components, characterize the structure and use of basic analog and digital electronic circuits - [K_W04 + K_W07 + K_W14 +++]
2. He can describe the basic criteria for the design of electronic circuits - [K_W04 + K_W14 +++]

Skills:

1. He can apply his knowledge of electronics to analyze the operation of basic analog and digital electronic circuits - [K_U01 + K_U03 ++]

Social competencies:

1. He can think and act in an entrepreneurial manner in the area of electronic design - [K_K02 ++]

Assessment methods of study outcomes

Assessment of the knowledge and skills shown on the written examination of a test and problematic

Course description

The properties and characteristics of the basic elements and electronic devices: passive components, p-n junction, diodes, transistors and their operation and application. Semiconductor optoelectronic devices - properties and application examples. Power rectifiers. Feedback in analog circuits. Operational amplifiers: ideal, real, properties, performance, and applications. Power amplifiers: classification, properties, and applications. Signal generators: generation conditions, types, and application of generators. Analog Filters: types, designing and aplication. Basics of digital technology: the binary system of writing numbers, logic states and logical operations: introduction (elements of logic, logic, truth table), digital circuits and sequential combination. The use of digital circuits. The TTL family. Semiconductor memory - general classification and properties of some types of memory.

Basic bibliography:

1. W. Golde, Układy elektroniczne, Wydanie drugie, WNT, Warszawa, 1974
2. Z. Kulka Z., M. Nadachowski, Analogowe układy scalone, WKŁ, W-wa 1980
3. P. Horowitz, W. Hill, Sztuka elektroniki, t. I, II, WKŁ, 1997
4. J. Kalisz, Podstawy techniki cyfrowej, WKiŁ, Warszawa 1998

Additional bibliography:

1. U. Tietze, Ch. Schenk, Układy półprzewodnikowe, WNT, 1996

Result of average student's workload

Activity	Time (working hours)
1. Participation in lecture classes	20
2. Participation in consultations	4
3. Preparation for the exam	8

Student's workload

Source of workload	hours	ECTS
Total workload	32	3
Contact hours	24	1
Practical activities	0	0